

A close-up photograph of a dandelion seed head, showing the intricate structure of the seeds and the central receptacle. The image is in grayscale, with a green rectangular overlay on the right side.

ROUTING IN FAT TREES ENGINEERING SIMPLICITY IN IP FABRIC

MELCHIOR AELMANS
MELCHIOR@JUNIPER.NET

JUNIPER
NETWORKS | Engineering
Simplicity



AGENDA

- IP FABRICS: THE BIG PICTURE
 - STANDARDIZED FORM-FACTOR OF NETWORK CAPACITY
 - NEXT-GEN EVOLUTION DRIVERS
- NEXT GEN UNDERLAY ROUTING PROTOCOL REQUIREMENTS
- RIFT BASIC CONCEPT
- RIFT ADVANTAGES
- STANDARDS STATUS
- PRODUCT STATUS



THE BIG PICTURE

NEXT GEN IP FABRIC EVOLUTION DRIVERS

IP FABRICS: THE BIG PICTURE

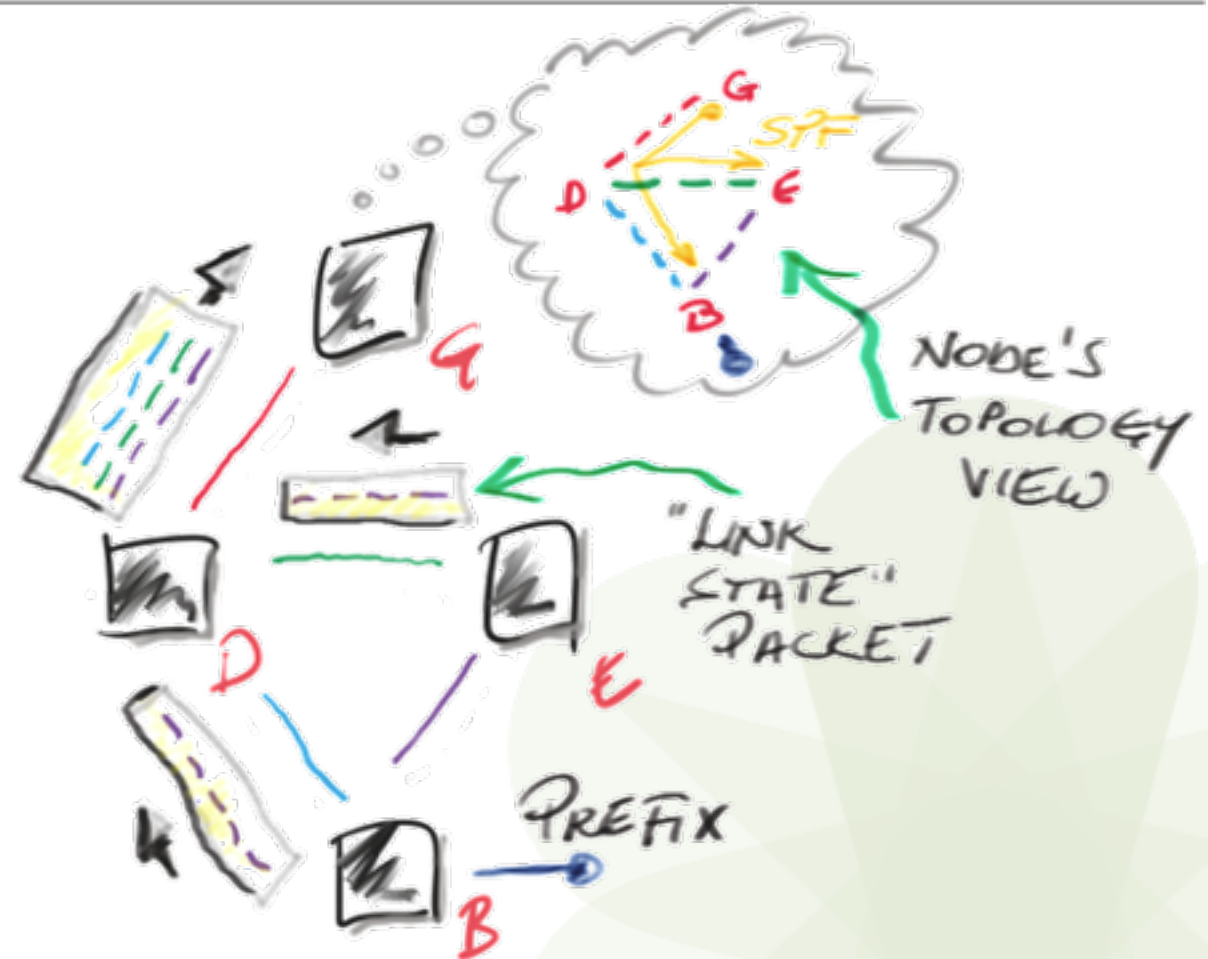
- THE GLOBAL DATA CENTER MARKET SIZE IS EXPECTED TO REACH REVENUES OF AROUND \$174 BILLION BY 2023
 - GROWING AT A CAGR OF ABOUT 4% 2018–2023
- TOPOLOGIES & TECHNOLOGIES ARE UNIFYING TOWARDS A “IP FABRIC SUBSTRATE” IN PROXIMITY TO PRODUCERS AND CONSUMERS OF CONTENT
- FORTUNE 1000 GENERATE AND HOST CONTENT AND ARE BUILDING OWN IP FABRICS TO CONSUME LARGER AND LARGER AMOUNTS OF “LOCAL” NETWORKING CAPACITY
- BUILDING IP FABRICS TODAY IS LARGELY HIGH OPEX ARTISANAL ACTIVITY
- MOST FORTUNE 1000 NEED A “STANDARDIZED FORM FACTOR” FOR BANDWIDTH JUST LIKE STORAGE
- WE LACK A STANDARDIZED ZTP UNDERLAY TECHNOLOGY TO MAKE IP FORWARDING IN FABRICS AS SIMPLE AS ETHERNET SWITCHING

WHAT AND WHY ?

- HYPER-SCALERS ARE EXTRAPOLATING THE THINGS TO COME
 - VAST AMOUNT OF BANDWIDTH CLOSE TO PRODUCER & CONSUMER ARE PROVISIONED
 - IP FABRICS IN DC (SERVER FARMS)
 - METRO (CACHES AND ACCESS)
 - DISAGGREGATED CHASSIS ARCHITECTURES IN BACKBONE WITH REGULAR FABRICS HOLDING LEAVES TOGETHER
 - THOSE TOPOLOGIES ARE UNIFORM, LOCAL AND REGULAR
 - AT THE END, ECONOMICS OF INTERCONNECTING OF CROSSBARS DONE IN 1950 IN BELL LABS ARE STILL VALID
 - WAN-STYLE TRAFFIC ENGINEERING & PROTECTION IS BEING REPLACED BY WIDE FAN-OUT & DISTRIBUTED SYSTEMS REDUNDANCY (RATHER THAN CHASSIS & FRR)
 - SIMPLER IS CHEAPER IN OPEX _AND_ CAPEX
 - HYPER-SCALERS ARE BUILDING CUSTOMIZED HIGH-OPEX SOLUTIONS TO MANAGE THOSE FABRICS
- IP FABRIC IS BECOMING THE NEW “RAM CHIP” TO CONSUME BANDWIDTH
 - NO ONE CONFIGURES SSD WEAR-LEVELING, RAM BANKS AND CAS/RAS MANUALLY IN EVERY LAPTOP
 - IP FABRICS HW IS LARGELY COMMODITY ALREADY
 - L3 FORWARDING IS THE NEW L2
 - IP FABRICS MUST AND WILL “OPEX COMMODITIZE”
- CUSTOMERS ARE HOSTING THEIR CONTENT & CRITICAL BUSINESS PROCESSES
 - HYBRID CLOUD FOR MANY REASONS, ONE OF THEM TO KEEP REAL-ESTATE FROM HYPER-SCALERS
 - NEED TO BUILD OWN FABRICS
 - HARD TO **SUSTAIN** PROPRIETARY OPEX EFFORTS

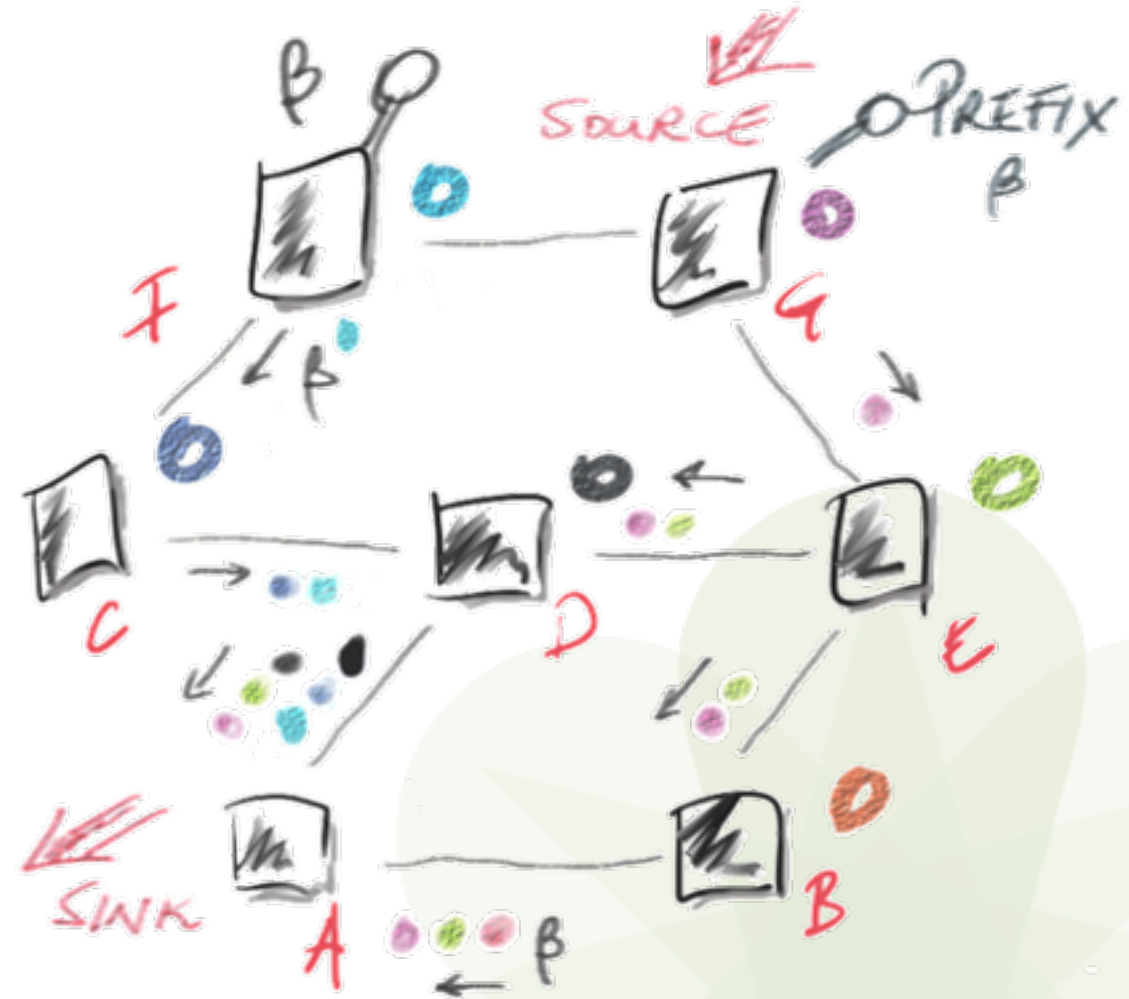
LINK STATE AND SPF = *DISTRIBUTED* COMPUTATION

- TOPOLOGY ELEMENTS
 - NODES
 - LINKS
 - PREFIXES
- EACH NODE ORIGINATES PACKETS WITH ITS ELEMENTS
- PACKETS ARE "FLOODED"
- "NEWEST" VERSION WINS
- EACH NODE "SEES" WHOLE TOPOLOGY
- EACH NODE "COMPUTES" REACHABILITY TO EVERYWHERE
- CONVERSION IS VERY FAST
- EVERY LINK FAILURE SHAKES WHOLE NETWORK (MODULO AREAS)
- FLOODING GENERATES EXCESSIVE LOAD FOR LARGE AVERAGE CONNECTIVITY
- PERIODIC REFRESHES (NOT STRICTLY NECESSARY)



DISTANCE/PATH VECTOR = *DIFFUSED COMPUTATION*

- PREFIXES "GATHER" METRIC WHEN PASSED ALONG LINKS
- EACH SINK COMPUTES "BEST" RESULT AND PASSES IT ON (ADD-PATH CHANGED THAT)
- A SINK KEEPS ALL COPIES, OTHERWISE IT WOULD HAVE TO TRIGGER "RE-DIFFUSION"
- LOOP PREVENTION IS EASY ON STRICTLY UNIFORMLY INCREASING METRIC
- IDEAL FOR "POLICY" RATHER THAN "REACHABILITY"
- SCALES WHEN PROPERLY IMPLEMENTED TO MUCH HIGHER # OF ROUTES THAN LINK-STATE



CURRENT STATE OF AFFAIRS

- SEVERAL OF LARGE DC FABRICS USE E-BGP WITH BAND-AIDS AS DE-FACTO IGP (RFC7938)
 - NUMBERING SCHEMES TO CONTROL “PATH HUNTING”
 - “LOOPING PATHS” (ALLOW-OWN-AS UNDER AS PRIVATE NUMBERING)
 - “RELAXED MULTI-PATH ECMP” SINCE ECMP OVER DIFFERENT AS IN EBGP DOES NOT WORK NORMALLY
 - ADD PATHS TO SUPPORT MULTI-HOMING, N-ECMP, PREVENT OSCILLATIONS
 - EFFORTS TO GET AROUND 65K ASes AND LIMITED PRIVATE AS SPACE
 - PROPRIETARY PROVISIONING AND CONFIGURATION SOLUTIONS, LLDP EXTENSIONS
 - “VIOLATIONS” OF FSM LIKE RESTART TIMERS AND MINIMUM-ROUTE-ADVERTISEMENT TIMERS
 - EMERGING WORK FOR “PEER AUTO-DISCOVERY” AND “SPF” DIAMETRICALLY OPPOSITE TO BGP DESIGN PRINCIPLES
 - RELIANCE ON “UPDATE GROUPS” ~ PEER GROUPS TO PREVENT WITHDRAWAL AND PATH HUNTING AFTER SERVER LINK FAILURES
- OTHERS RUN IGP (ISIS)
 - GENERALLY A “BETTER” APPROACH TO FASTER CONVERGENCE
 - CURRENT ATTEMPTS TO DEAL WITH SOME “SPOT PROBLEMS” LIKE FLOODING REDUCTION
- YET OTHERS RUN BGP OVER IGP (TRADITIONAL ROUTING ARCHITECTURE)
- LESS THAN MORE SUCCESSFUL ATTEMPTS @ PREFIX SUMMARIZATION, MICRO- AND BLACK-HOLING, BLAST RADIUS CONTAINMENT
- SERVER MULTI-HOMING NOT POSSIBLE USING IP DUE TO EQUAL COST AND SCALING CONSTRAINTS, HENCE MC-LAG’ED SOLUTIONS OR EVPN
- IN SUMMARY: HIGH OPEX SOLUTIONS NOT NECESSARILY VIABLE FOR CUSTOMERS WHO CANNOT OR DO NOT WANT TO BUILD SOPHISTICATED TALENT POOL TO DEAL WITH THEIR “UNICORN” FABRICS



NEXT GEN IP FABRIC UNDERLAY ROUTING PROTOCOL REQUIREMENTS

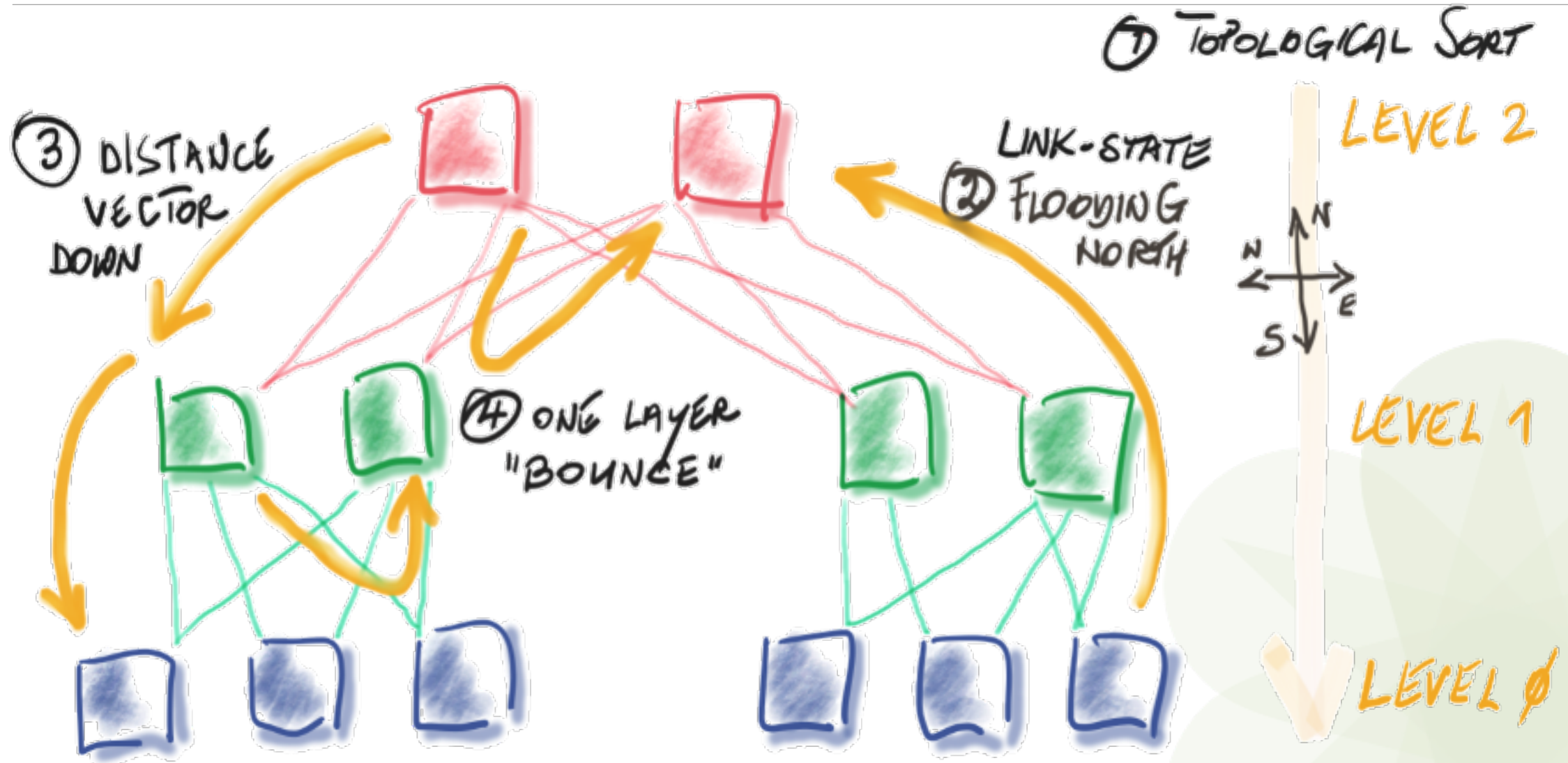
NEXT GEN UNDERLAY PROTOCOL REQUIREMENTS

Problem / Attempted Solution	BGP modified for DC (all kind of "mods")	ISIS modified for DC (RFC7356 + "mods")	RIFT Native DC
Peer Discovery/Automatic Forming of Trees/Preventing Cabling Violations	⚠	⚠	✓
No Need for Internal Addressing, Minimal Amount of Routes/Information on ToRs, Stretches True Routing to the Multi-Homed Host	✗	✗	✓
High Degree of ECMP (BGP needs lots knobs, memory, own-AS-path violations) and ideally NEC and LFA	⚠	✓	✓
Non Equal Cost Multi-Path, ECMP Independent Anycast, MC-LAG Replacement	✗	✗	✓
Traffic Engineering by Next-Hops, Prefix Modifications	✓	✗	✓
See All Links in Topology to Support PCE/SR	⚠	✓	✓
Carry Opaque Configuration Data (Key-Value) Efficiently	✗	⚠	✓
Take a Node out of Production Quickly and Without Disruption	✗	✓	✓
Automatic Disaggregation on Failures to Prevent Black-Holing and Back-Hauling	✗	✗	✓
Minimal Blast Radius on Failures (On Failure Smallest Possible Part of the Network "Shakes")	✗	✗	✓
Fastest Possible Convergence on Failures	✗	✓	✓
State of the Art Security Including Originator Validation and Replay Prevention	✗	✗	✓
Simplest Initial Implementation	✓	✗	✓✗



RIFT BASIC CONCEPT

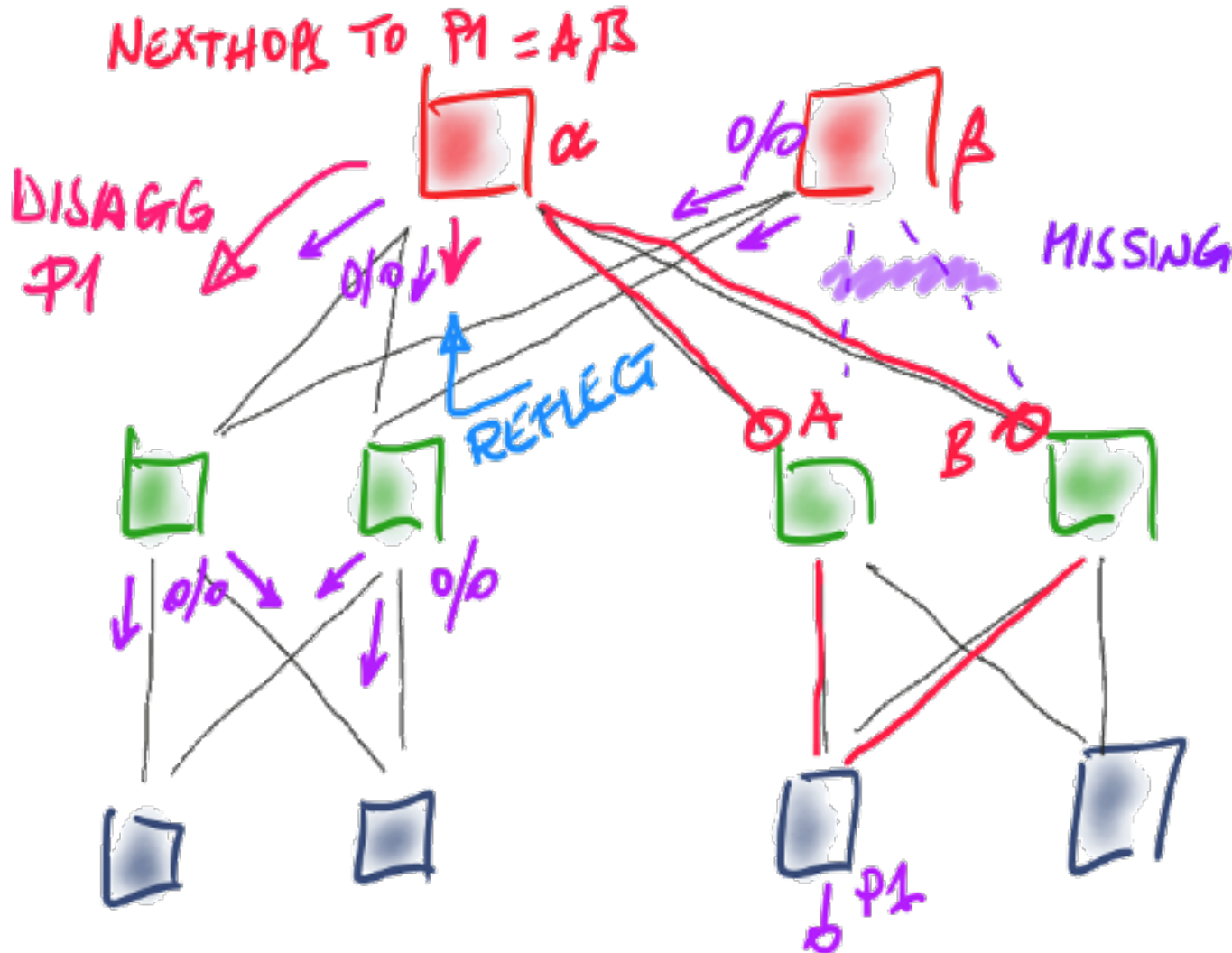
RIFT CONCEPT





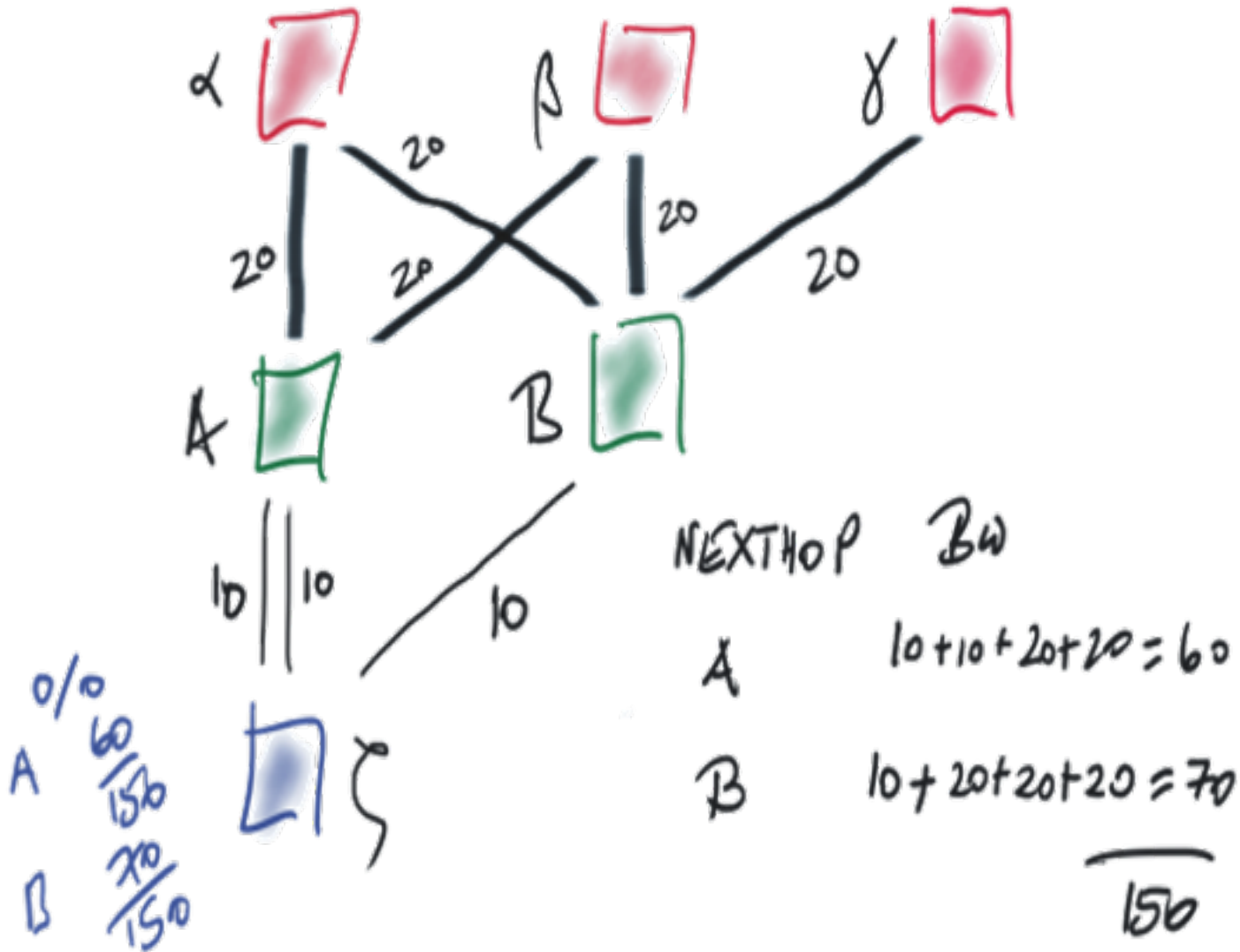
RIFT ADVANTAGES

AUTOMATIC DE-AGGREGATION



- SOUTH REPRESENTATION WITH ADJACENCIES OF RED BETA IS REFLECTED BY THE GREEN LAYER TO ALPHA
- ALPHA RED SPINE COMPUTES P1 REACHABILITY (TREE THAT INDICATES NEXT-HOPS)
- ALPHA SEES THAT BETA HAS NO ADJACENCIES TO ANY OF THE AVAILABLE NEXT-HOPS TO P1
- ALPHA DISAGGREGATES P1
- LEFT GREEN NODES DO NOT PROPAGATE DISAGGREGATION FURTHER
- WHEN BETA OBTAINS A VIABLE NEXT-HOP TO P1, ALPHA AUTOMATICALLY REAGGREGATES

NORTHBOUND BANDWIDTH BALANCING



- ZETA COMPUTES BANDWIDTH SUM TO NEXTHOP AND NEXT LEVEL UP
- THROUGH A CUMULATIVE 60 UNITS
- THROUGH B CUMULATIVE 70 UNITS
- DEFAULT ROUTE USES THAT IN SOME FORM TO UNEQUAL LOAD BALANCE
- PERFECTLY SAFE SINCE RIFT IS LOOP-FREE

OPERATIONAL ADVANTAGES

- Open IETF Standard
 - Can Build Hybrid Vendor Fabrics
 - Protocol is Well Reviewed and Understood by World-Class Experts
- True ZTP
 - No Configuration Necessary
 - V4 over V6 Forwarding
 - Mis-cabling Handled
- Can Operate on Asymmetric Bandwidth Fabrics and Handle “Fat Link” Failures By Adjusting Automatically
- Can Support and Scale to an Architecture with Multi-Home Servers
 - No Need for Service Migration on ToR Upgrades
 - Can Talk Directly to Hyper-Visors/Kubernetes GW
- BFD is “Built In”
 - Can Be Used for Fast Rehash or Early Loss Detection
- Runs on UDP
 - Trivial Kernel Support on All Platforms
 - Allows for Max. Speed Flooding
 - Easy to “Multi-Instantiate” for Different Purposes
- Minimal Blast-Radius
 - Failures/Bring-Up on Fabric Only Affects the Smallest Viable Radius
- RIFT Flooding is ~30% of Normal Flat IGP
 - Built-In Flood Reduction Reduces Flood Traffic to <20% of Flat IGP
- Loop-Free
 - Can Utilize **All** Viable Paths Through Fabric
 - Can Support True Anycast
- Model Based
 - Much Less Possibility for Parser and Formatter Bugs Plaguing Today’s Networking Protocols
- Specification is Written for Maximum parallelization
 - With Enough Cores IP Switches Should Be Able to Converge @ Speeds Making FRR Unnecessary (Assuming Fast Rehash)
- KV Store Allows to Replace Out-Of-Band Applications
 - IP/MAC Binding Can Be Flooded to Top-of-Fabric
- Sophisticated, Newest Routing Security
- Full Fabric Visibility at the Top



STANDARDIZATION PROGRESS

STANDARDIZATION PROGRESS

RIFT STANDARDS TRACK IETF WORKING GROUP IN REVISION -12

- SECURITY AREA DIRECTORATE EARLY REVISION DONE AND ADDRESSED
- IESG REVISIONS BEING ADDRESSED
- CO-AUTHORS FROM CISCO, YANDEX, MELLANOX, HP & IMPLEMENTING INDIVIDUALS
- [HTTPS://TOOLS.IETF.ORG/HTML/DRAFT-IETF-RIFT-RIFT-12](https://tools.ietf.org/html/draft-ietf-rift-rift-12)

APPLICABILITY DRAFT

- CO-AUTHORED WITH OPERATORS, E.G. ORANGE AND YANDEX
- UNDER GEN-ART REVIEW
- [HTTPS://TOOLS.IETF.ORG/HTML/DRAFT-IETF-RIFT-APPLICABILITY-03](https://tools.ietf.org/html/draft-ietf-rift-applicability-03)



PRODUCT STATUS

PRODUCTIZATION

IMPLEMENTATIONS

JUNIPER NETWORKS

- FCS IN 19.4R1 64-BIT JUNOS
- PACKAGE INSTALLING OVER 19.4R1 OR NEWER
- QFX (ALL VARIANTS), VMX, MX
- CRPD & EVO IN PROGRESS
- <https://www.juniper.net/us/en/dm/free-rift-trial/>

PYTHON OPEN-SOURCE BY BRUNO RIJSMAN

- <https://github.com/brunorijsman/rift-python>

DOCUMENTATION

- DAY ONE BOOK: ROUTING IN FAT TREES (RIFT)
 - COVERING JUNIPER AND OPEN-SOURCE IMPLEMENTATION
 - RIFT WIRESHARK DISSECTOR
 - DOWNLOAD FREE: [HTTP://JNPR.NL/RIFT](http://jnpr.nl/rift)

JUNIPER | Engineering
networks | Simplicity

DAY ONE: ROUTING IN FAT TREES (RIFT)



By Melchior Aelmans, Olivier Vandezande, Bruno Rijzman,
Jordan Head, Christian Graf, Leonardo Alberro,
Hitesh Mali, Oliver Stuedler



RIFT: ENGINEERING SIMPLICITY IN IP FABRIC QUESTIONS?

MELCHIOR AELMANS
MELCHIOR@JUNIPER.NET

JUNIPER
NETWORKS | Engineering
Simplicity